

PATENT

DOCKET NO.: 2207/10730

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : John I. GARNEY

SERIAL NO. : 09/823,455

FILED : March 30, 2001

FOR : METHOD AND APPARATUS FOR IMPROVING TIME  
CONSTRAINTS AND EXTENDING LIMITED LENGTH  
CABLES IN A MULTIPLE-SPEED BUSGROUP ART UNIT : ~~2189~~ 2112  
EXAMINER : SN 6/4/04  
Sumati LEFKOWITZ**RECEIVED**

JUN 10 2004

Technology Center 2100

M/S: AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING	
Hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail and deposited with the U.S. Postal Service and addressed to: M/S AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on	
Dated: June 4, 2004	Signature: Barbara Vance
	Barbara Vance

DECLARATION

Sir:

1. I, John I. Garney, hereby declare as follows:

2. I am the inventor of a method and apparatus for improving time constraints and extending limited length cables in a multiple speed bus that are described and claimed in U.S. Application Serial No. 09/823,455 filed on March 30, 2001.

3. I understand that certain sections of Universal Serial Bus Specification, Revision 2.0 have been cited against my patent application. The publication date for this reference is April 27, 2000. This document is hereinafter referred to as "USB 2.0".

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Intel 10730 Declaration of John Garney.DOC

4. In the most recent Office Action, the Examiner cited the following sections of the USB 2.0 in rejecting claims of the present patent application:

- a. Page 344 – Use of a FIFO as a storage element;
- b. Page 297 – Use of a transaction translator as a data forwarding element;
- c. Pages 199-204 – Section 8.4.2 on split transactions;
- d. Page 205 – Defines microframes; and
- e. Pages 372-377 – Section 11.18 on split transactions.

5. I conceived these elements of the invention claimed in the present application prior to April 27, 2000.

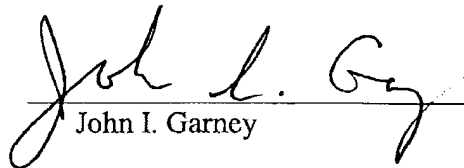
6. I am an author of several sections of the USB 2.0.

7. With respect to all of the cited sections of the USB 2.0 listed above, I conceived these elements of the invention before these sections were drafted.

8. I was personally responsible for writing the relevant text that appears on Pages 344, 297, 199-204 (Section 8.4.2), 205, and 372-377 (Section 11.18) of the USB 2.0.

9. I hereby further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Dated: 6/4/2004

  
John I. Garney

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